embedded VISIMN Summit

Parallelizing machine learning application in the cloud with Kubernetes: A case study

Rajy Rawther, Advanced Micro Devices September 2020

Agenda

embedded VISION Summit

- ML application end-to-end flow
- Kubernetes: A brief introduction
- Scaling image classification in the cloud using Kubernetes
- Scaling inference from one server node to many
- Load balancing and identifying bottlenecks in the deployment pipeline
- How to choose between CPUs or GPUs for performance
- Conclusion
 - Kubernetes is available at https://github.com/kubernetes/kubernetes and licensed under Apache 2.0

ML application end-to-end flow



• CPUs/GPUs or dedicated

- With right software infrastructure
- Data collection is tedious and quality of data matters
- Need synthetic data generation with tools for augmentation
- Start with simple model, train and update for accuracy
- Need many CPU and GPU cores to run these in a short amount of time
- Efficient data pipeline is required to get optimal throughput
- When the model is given new data, there is a need to evaluate and update the model for accuracy.



ML Software stack for application deployment





Kubernetes[®](K8s): A brief introduction



Kubernetes is an open-source system for automatic deployment, scaling and management of containerized applications



- Node
 - Runs Kubelets ("node agent" service)
 - Communicates with master
 - Runs Pods
- Pod
 - Runs one or more containers
 - Exists on a node
- Service
 - Handles requests
 - Load balances
- Deployment
 - Defines what you want(cluster services); Kubernetes handles it for you

[AMD Public Use]

Need for containerized and scalable deployment





Typical deployment YAML file for Kubernetes configuration

Deployment YAML configuration

t deployment yml file	
apiVersion: apps/v1	
cind: Deployment	
netadata:	
name: mivisionx-deployment	
labels:	
app: mivisionx-server	
spec:	
replicas: n #number of replicas	
selector:	
matchLabels:	
app: mivisionx-server	
template: # define the pods specifications	
metadata:	
Labels:	
app: mivisionx-server	
spec:	
containers:	
- name: mivisionx-server	
mage: mivisionx/ubuntu-18.04:rocm3.3	
ports:	
- ContainerPort. 20202	
- name: HTP_VISTRLE_DEVICES	
$value \cdot "0" \pm \pm 0.12 \qquad n \text{ for GPU} = 1 \text{ for } 0$	^DI I
command: ["/bin/sh", "-c", ""]	
resources:	
limits:	
amd.com/gpu: 1 # requesting 1 GPU	
The second sec	



Service YAML configuration

Version: v1 d: Service
adata: ame: mivisionx-deployment-service amespace: default
vne: NodePort
elector:
<mark>⊣≻app:</mark> mivisionx-server orts:
<pre>- port: 28282 #port accessible inside the cluster</pre>
targetPort: 28282 #port which sends traffic from service to container nodePort: 30001 #port which is accessable outside the cluster protocol: TCP labels: app: mivisionx-server



Load balancing a deployment pipeline with CPU and GPU





Data-parallel processors are efficient for running inference on a batch of images

Postprocessing is needed to produce useful result

Inference deployment client server application case study

Server Node В **Client Application** Model and Parameters Initialize Inference (Compile, build inference graph, 1. Choose model Status set-up hardware) & parameters А Setup Phase Image 2. Choose dataset CPU database **GPU #0** cores D С GPU #1 Images Image decode GPU #2 G Image Transform **Results** 3. View results GPU #3 Results Ε **Inference Execution** Multi-GPU inference A-G Critical path flow Up to 8 GPUs on a single server node

embedded

Inference Deployment Using Kubernetes





Scaling ML Inference with Kubernetes®



ML Inference

24 Kubernetes[®] Pods Accelerated by 24x GPUs







Performance Graph with multiple GPUs and CPUs





embedded

Steps for achieving linear scaling



- Remove bottlenecks in the inference server critical path.
- Allocate hardware resources for each deployment pod. In this case we choose 1 GPU and 8 CPU cores
- The application needs to maintain separate queues (as shown in the next slide) for each instance of application so multiple instances won't block each other.
- The model is pre-launched and initialized for each nod separately
- The data loading bottleneck is avoided by preloading input images for each nod in advance
- Finally, use a multi-threaded client application that feeds and sends requests for each of the K8s pod with minimal latency.

Inference server critical path





Server processing queues

embedded sum



© 2020 Advanced Micro Devices

Overall balance of different stages of pipeline



Classification pipeline stages, potential FPS



Kubernetes Pros and Cons





- Deep integration into cloud native ecosystem
- Broad support for containers and runtimes
- Automatic scaling and load balancing
- Efficient resource management
- Multiple workloads and deployment options
- Built-in security
- Integration with major cloud providers



- Steep learning curve
- Challenging to install and configure manually
- Not suited for simple applications and can reduce productivity adopting it
- Need expensive talent to adopt it

Next Steps: Collecting metrics and evaluating the results

VISICN Summit

- Each K8s container can collect metrics asynchronously.
- Evaluating the neural network model guarantees the model will perform well given new data
- Various tools can be used to evaluate model for a given dataset
- E.g., inference analyzer to validate different models on one or more dataset



Final Thoughts

embedded VISICN Summit

- Intelligent load balancing between CPU and GPU can increase the overall throughput
- By carefully analyzing the input pipeline and identifying parts that affect the gross performance, the latency can be minimized
- Different image sizes and complex models tip the workload to weigh on CPU/GPU or data-parallel processors
- Maintaining data queues at various stages is essential to reduce data-transfer bottlenecks
- Smaller batch sizes lower latency but doesn't give the best performance
- Finally, having the right tools to visually analyze the results is key to understand the whole picture



Conclusion







References



MIVisionX

https://github.com/GPUOpen-ProfessionalCompute-Libraries/MIVisionX

Kubernetes

https://kubernetes.io/docs/home/

ResNet

https://github.com/onnx/models/tree/master/ vision/classification/resnet



Disclaimer



The information presented in this document is for informational purposes only and may contain technical inaccuracies, omissions, and typographical errors. The information contained herein is subject to change and may be rendered inaccurate for many reasons, including but not limited to product and roadmap changes, component and motherboard version changes, new model and/or product releases, product differences between differing manufacturers, software changes, BIOS flashes, firmware upgrades, or the like. Any computer system has risks of security vulnerabilities that cannot be completely prevented or mitigated. AMD assumes no obligation to update or otherwise correct or revise this information. However, AMD reserves the right to revise this information and to make changes from time to time to the content hereof without obligation of AMD to notify any person of such revisions or changes.

THIS INFORMATION IS PROVIDED 'AS IS." AMD MAKES NO REPRESENTATIONS OR WARRANTIES WITH RESPECT TO THE CONTENTS HEREOF AND ASSUMES NO RESPONSIBILITY FOR ANY INACCURACIES, ERRORS, OR OMISSIONS THAT MAY APPEAR IN THIS INFORMATION. AMD SPECIFICALLY DISCLAIMS ANY IMPLIED WARRANTIES OF NON-INFRINGEMENT, MERCHANTABILITY, OR FITNESS FOR ANY PARTICULAR PURPOSE. IN NO EVENT WILL AMD BE LIABLE TO ANY PERSON FOR ANY RELIANCE, DIRECT, INDIRECT, SPECIAL, OR OTHER CONSEQUENTIAL DAMAGES ARISING FROM THE USE OF ANY INFORMATION CONTAINED HEREIN, EVEN IF AMD IS EXPRESSLY ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

© 2020 Advanced Micro Devices, Inc. All rights reserved.

AMD, the AMD Arrow logo, Epyc, Radeon, ROCm and combinations thereof are trademarks of Advanced Micro Devices, Inc. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies.

