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Intelligent Vision for the Industrial, Automotive and IoT Edge with the i.MX 8M Plus Applications Processor

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• i.MX 8M Plus machine vision features

• Introduction to i.MX 8M Plus SoC

Introduction to NXP applications processors

• ISP

Outline

- NPU
- AI ML and machine vision software enablement

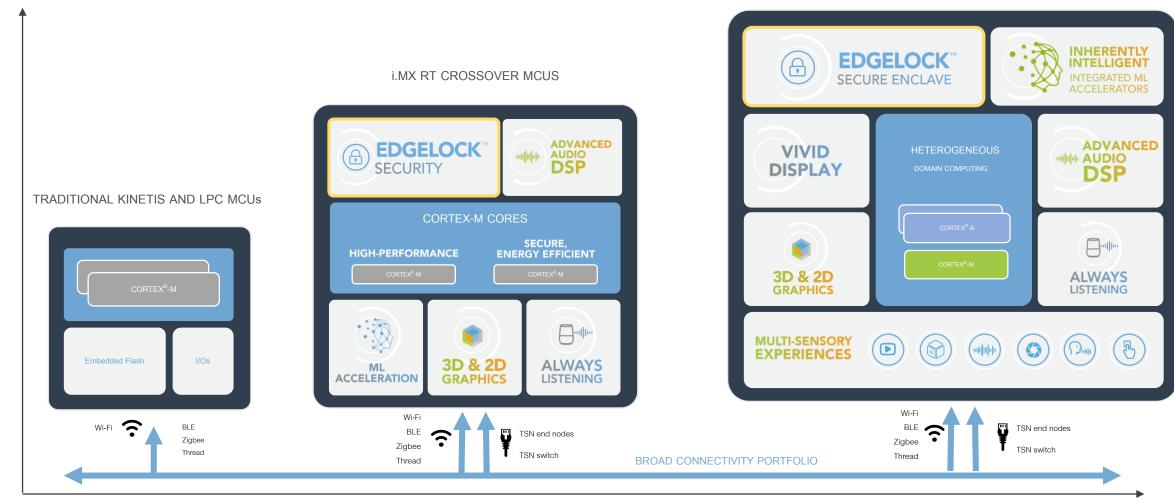




Scalable Compute Platforms

i.MX & LAYERSCAPE® APPS PROCESSORS

FUNCTIONAL INTEGRATION



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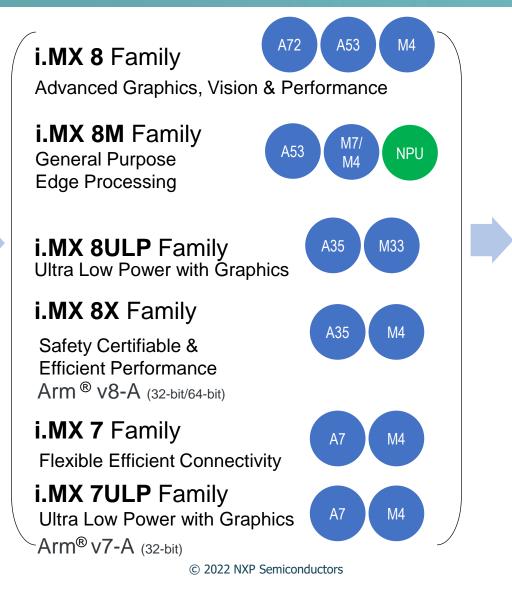
PERFORMANCE

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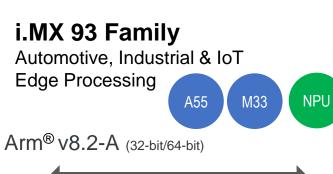
i.MX Series of Applications Processors

i.MX 6QuadPlus Pin-to-pin Compatible i.MX 6Quad A9 i.MX 6DualPlus i.MX 6Dual Compatible i.MX 6DualLite i.MX 6Solo Software A9 M4 i.MX 6SoloX i.MX 6SoloLite i.MX 6SLL i.MX 6UltraLite i.MX 6ULL A7 i.MX 6ULZ Arm[®] v7-A



Future i.MX 9 Families

Automotive, Industrial & IoT Edge Processing



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i.MX Series of Applications Processors

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i.MX 6

- 12 product families
- Offers software and pin-pin compatibility
- Arm[®] v7-A



Advanced Graphics, Vision & Performance

M7/

NPU

A53

i.MX 8M Family General Purpose Edge Processing



i.MX 8X Family

Safety Certifiable & Efficient Performance Arm ® v8-A (32-bit/64-bit)

i.MX 7 Family Flexible Efficient Connectivity

i.MX 7ULP Family Ultra Low Power with Graphics ~Arm[®] v7-A (32-bit)

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i.MX 8M Plus

2.3 TOPS NPU, Vision (ISP), 1080p video encode/decode, advanced HMI, multi-display, USB3/2xGbE/PCIe

i.MX 8M

4K HDR, 4K video decode, advanced HMI, multi-display USB3/GbE/2xPCIe

Pin-to-pin Compatible

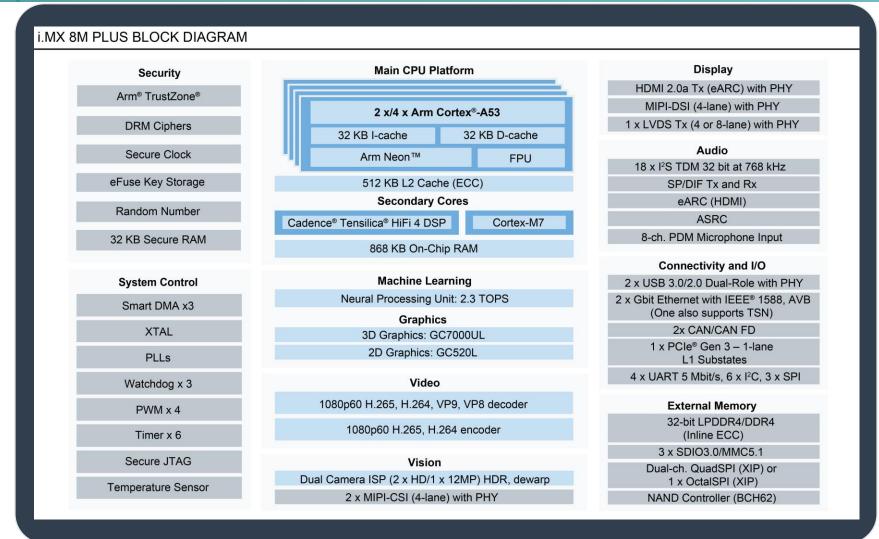
i.MX 8M Mini

Versatile multimedia applications processor with 1080p video encode/decode, HMI graphics acceleration, USB/GbE/PCIe

i.MX 8M Nano

Entry-level multimedia applications processor with HMI graphics acceleration, USB/GbE

i.MX 8M Plus Applications Processor with ML & Vision Engines





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i.MX 8M Plus Applications Processor with ML & Vision Engines

4x Cortex® A53 @ 1.8GHz

- Speech recognition
- Object detection
- Gesture recognition

HiFi4 DSP @ 800MHz

- Voice/Keyword recognition
- Speech enhancement
- Noise reduction

GC7000UL 3D Graphics Engine (GPU) @ 1GHz

- Object detection classification
- Floating point support

MX 8M PLUS BLOCK DIAGRAM			
Security	Main CPU Platform		
Arm [®] TrustZone [®]	[HDMI 2.0	
Ann Hustzone	2 x/4 x Arm Cortex [®] -A53	MIPI-DS	
DRM Ciphers	32 KB I-cache 32 KB D-cache	1 x LVDS T	
Secure Clock			
	Arm Neon™ FPU	18 x I²S T	
eFuse Key Storage	512 KB L2 Cache (ECC)	SP	
Random Number	Secondary Cores	e	
Random Hambor	Cadence® Tensilica® HiFi 4 DSP Cortex-M7		
32 KB Secure RAM	868 KB On-Chip RAM	8-ch. PD	
		Con	
System Control	Machine Learning	2 x USB 3.0	
Smart DMA x3	Neural Processing Unit: 2.3 TOPS	2 x Gbit Ether	
X-TAL	Graphics	(One a	
XTAL	3D Graphics: GC7000UL	1 x PC	
PLLs	2D Graphics: GC520L		
Watchdog x 3	Video	4 x UART	
-	1080p60 H.265, H.264, VP9, VP8 decoder	Ext	
PWM x 4		32-	
Timer x 6	1080p60 H.265, H.264 encoder		
Secure JTAG	Vision	3 x S	
	Dual Camera ISP (2 x HD/1 x 12MP) HDR, dewarp	Dual-ch 1 x	
Temperature Sensor	2 x MIPI-CSI (4-lane) with PHY	NAND	

Cortex-M7 @ 800MHz

Keyword detection

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• Sensor fusion

Display

Audio TDM 32 bit at 768 kHz P/DIF Tx and Rx

eARC (HDMI) ASRC

OM Microphone Inpu

0/2.0 Dual-Role with PHY

rnet with IEEE® 1588, AVB also supports TSN)

nectivity and I/O

x CAN/CAN FD

L1 Substates 5 Mbit/s, 6 x I²C, 3 x SP

xternal Memory 2-bit LPDDR4/DDR4

(Inline ECC) SDIO3.0/MMC5.1 h. QuadSPI (XIP) or

OctalSPI (XIP)

Controller (BCH62)

Cle® Gen 3 – 1-lane

0a Tx (eARC) with PH OSI (4-lane) with PHY

Tx (4 or 8-lane) with PHY

Anomaly detection

Neural Processing Unit (NPU) @ 1GHz

 Multi-camera classification and detection

Two-channel Image Signal Processor (ISP)

 De-warping and image enhancement



i.MX 8M Plus Target Applications

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ML and Industrial Automation	Smart Home, Building & City	Consumer Audio/Voice Systems
 Machine vision and robot controller Industrial computer, gateways, HMI Printers and scanners Machine visual inspection Factory automation 	 Safety, security and surveillance Fleet analytics Traffic monitor and flow optimization Vision payment systems Targeted advertisement Service drones Alarm and AI server hubs Home patient and elderly monitor 	 Surround sound and sound bars Audio/video receiver Immersive audio products Wireless or networked smart speakers Personal assistant Voice-assisted products







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ISP (Image Signal Processor)

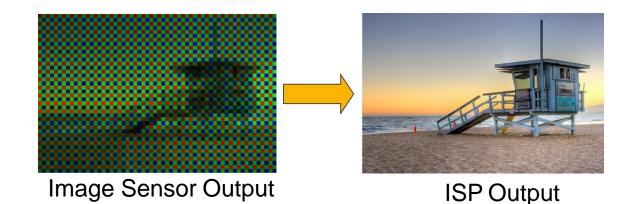




i.MX 8M Plus SoC: Why an Integrated ISP?

Image Signal Processor (ISP) basic function

- Converts the image color code from raw Bayer (output of the image sensor) to YUV so it can be processed by the SoC
- Provides additional processing to improve the image quality:
 - HDR extracts maximum image detail in high contrast scenes
 - De-Warp: Fisheye lens or low-cost lens geometry correction
 - Image Enhancement
- i.MX 8M Plus ISP Benefits:
 - Low latency and high performance
 - Lower BOM cost for vision system
 - Higher product longevity





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i.MX 8M Plus SoC: ISP Key Features

- Bayer de-mosaicing and filtering (including denoising, sharpening and blurring)
- Defect pixel cluster correction (DPCC)
- Color processor (CPROC)
- Chromatic aberration correction (CAC)
- Denoise
- Histogram
- Lens shading correction (LSC)
- Wide Dynamic Range (local tone mapping)
- Color noise removal (CNR)
- Automatic white balance measurements (AWB)
- Exposure measurement for AE (AEC/AGC)
- Auto focus measurement (AF)
- 2-exposure and 3-exposure DoL/Staggered HDR



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AL/ML NPU (Neural Processing Unit)

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i.MX 8M Plus NPU Subsystem

Programmable Engine Unit

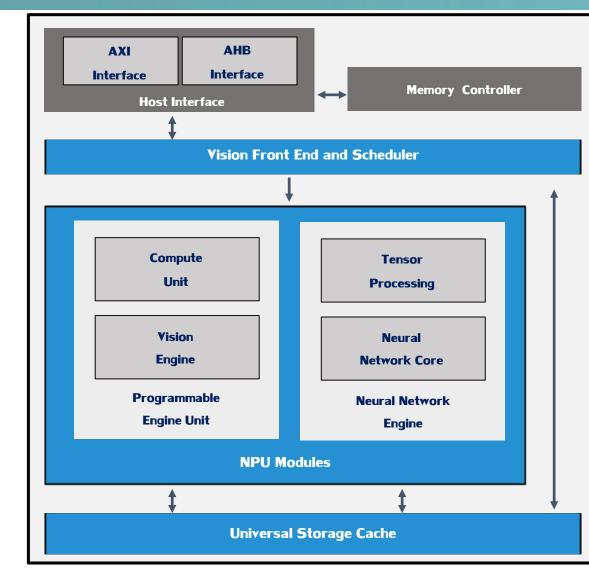
- 128-bit vector processing.
 INT 8/16/32b, FLOAT 16/32b.
- Most flexible programming
 unit

Vision Engine

Provides advanced image
 processing functions

Universal Storage Cache

 Local memory and L1 cache to pass data amongst NPU modules



Tensor Processing Core (3 instances)

- INT 8/16b, FLOAT 16b
- Non-convolution layers.
- Multi-lane processing for data shuffling, normalization,

pooling/unpooling, LUT, etc.

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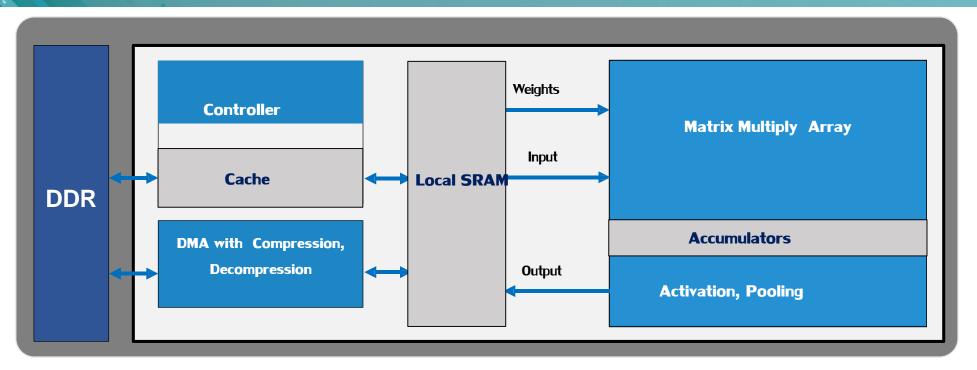
Network pruning support, zero skipping, compression

Neural Network Core (6 instances)

- 2.3 TOPs INT8
- Convolution Layers, RELU, Max Pooling, and Compute Bounded Fully Connected Layers



i.MX 8M Plus NPU Features



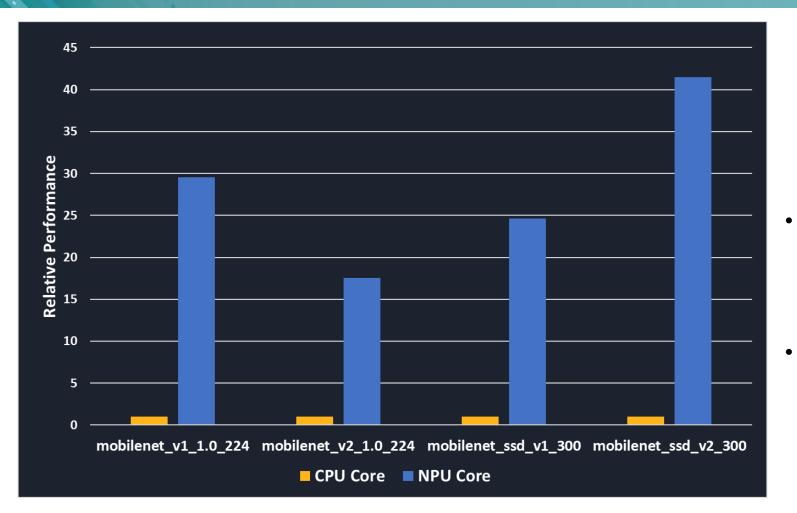
- Scalable 8- and 16-bit integer multiply-accumulate (MAC) engine for tensor operations
- Specialized NN hardware supports activation and pooling
- Supports variety of NN topologies:
 - Convolutional (CNN): MobileNet, YOLO, etc
 - Recurrent (RNN, GRU, LSTM): Deep Speech 2, etc.



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NPU Performance Increase for Quantized Models



- Measured on i.MX 8M Plus with 1 Cortex A53 CPU core vs. NPU normalized to 1 GHz for both cores
- Both A53 and NPU are 8-bit quantized



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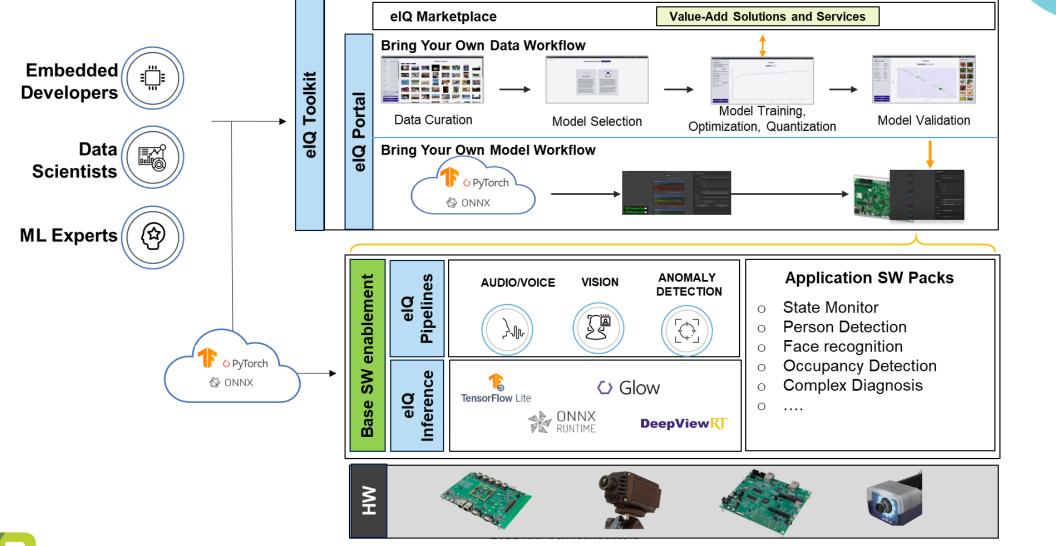


Machine Vision Software Enablement

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eIQ[®] ML Software Development Environment



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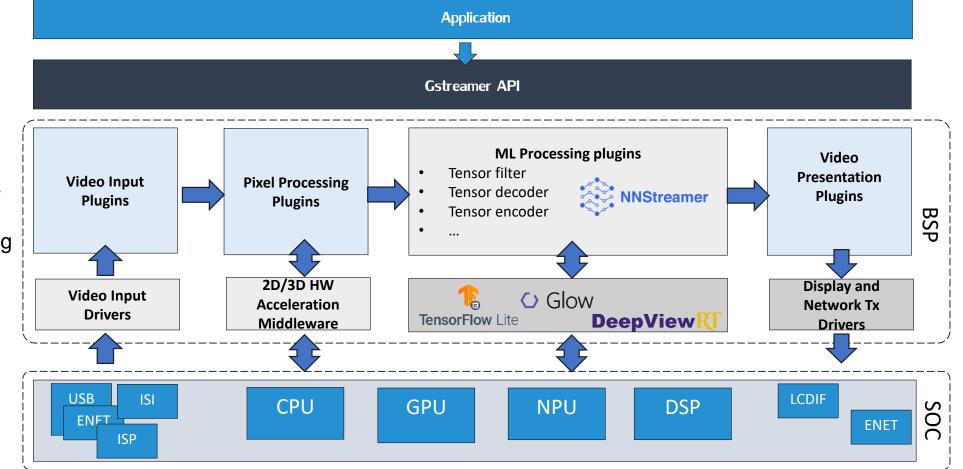
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i.MX 8M Plus Vision Pipeline

- **Gstreamer** offers an opensource industry-standard solution to handling media components for embedded devices.
- NNStreamer provides a set of GStreamer plugins allowing developers to apply neural networks, attach related frameworks (including ROS, IIO, FlatBuffers, and Protocol Buffers), and manipulate tensor data streams in GStreamer pipelines easily and execute such pipelines efficiently.





Gstreamer: https://gstreamer.freedesktop.org/ NNStreamer: https://nnstreamer.ai/ embedded

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Machine Vision Use Case

Available Demos



Multimedia

GStreamer

Video Test Source Camera Preview Camera using VPU Multi-Cam Preview

ISP ISP Control Demo Video Dump Demo

> Audio Audio Record Audio Play

GLES2 Vivante Launcher Cover Flow Vivante Tutorial Bloom Blur DF Graphics Basic 2D **Eight Layer Blend** Fractal Shader Line Builder 101 Model Loader S03 Transform S04 Projection

SO6 Texturing Mapping Mapping Refraction

> **OpenVG 2D** Tiger G2D

Not all demos listed are available on all boards Machine Learning

NNStreamer Object Classification Object Detection Pose Detection Brand Detection

PyelQ Object Classification Object Detection Mask Detection

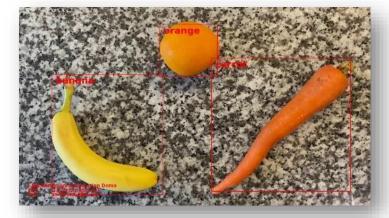


GPU

NNstreamer (ML Vision) Demos



- Object classification demo
- Object detection demo
- Pose detection demo
- Brand classification demo







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ISP Demos



- The "ISP control" demos allow users to control the following:
 - De-warp
 - Frames per second
 - Auto white balance
 - Color processing
 - De-mosaicing
 - Gamma control
 - Filtering
 - Black level subtraction
- New demo in 2022 Q1: users can use GUI to dump unprocessed camera data onto a USB drive



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The i.MX platforms are some of the most versatile applications processors families for multimedia and display applications

- The i.MX 8M Plus SoC offers
 - Camera capture, network and display drivers / frameworks
 - Multiple ML frameworks available, with multiple inference HW supported (NPU, GPU, CPU, DSP)
 - Multimedia accelerators (2D GPUs, 2.5D GPUs, 3D GPUs...) and industrystandard APIs
 - eIQ ML SW Development Environment
 - Reference code and demos targeting machine vision applications



Resources

Embedding Intelligence at the Edge

www.nxp.com/ai

i.MX 8M Plus product page:

www.nxp.com/imx8mplus

eIQ[®] ML Software Development Environment

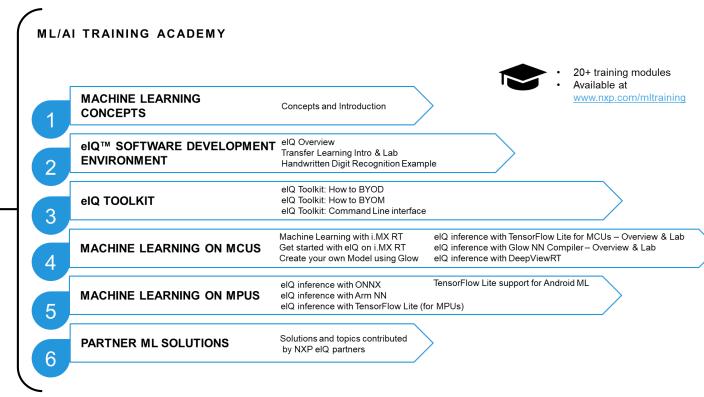
www.nxp.com/eiq

eIQ ML/AI Training Series

<u>www.nxp.com/mltraining</u>

NXP Application SW Packs

www.nxp.com/appswpack





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Thank You



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